1/3

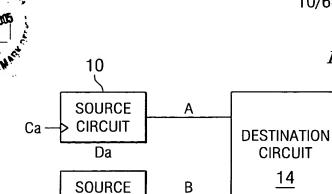


FIG. 1a

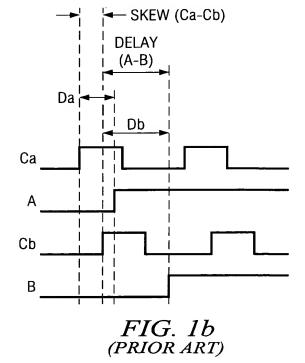
(PRIOR ART)

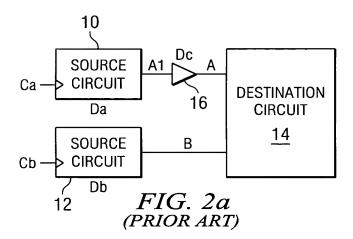
CIRCUIT

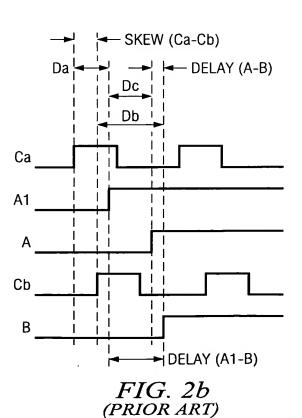
Db

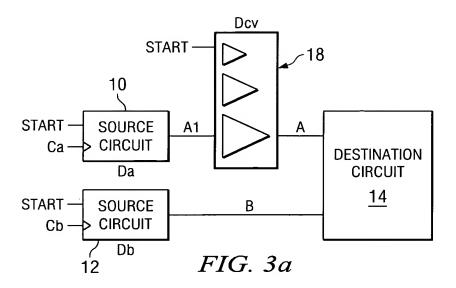
12

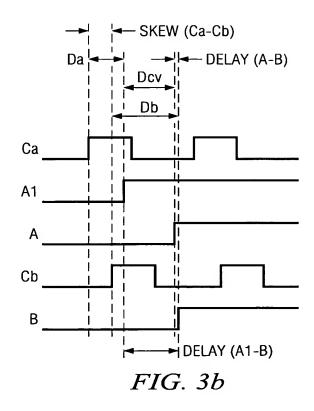
Cb-











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